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Attorney Docket No.: ELGP-6715-US

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Assaf Shapir                      Examiner:                      Nguyen, Dang T  
Serial No.:                      10/537,857                      Group Art Unit:                      2824  
Filed:                      06/07/2005  
Title:                      Method, circuit and system for erasing one or more non-volatile  
                                 memory cells

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**AMENDMENT**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This communication is filed in response to the Office Action dated September 21<sup>st</sup>, 2006 issued by the United States Patent and Trademark Office in connection with the above-identified Application. A response to the September 21<sup>st</sup>, 2006 Office Action is due December 21<sup>st</sup>, 2006. A petition for a one month extension of time, along with the associated fee are concurrently filed herewith. Accordingly, this communication is being timely filed.

Kindly amend the above-identified application as follows and consider the following remarks        **Amendments to the Drawings** begin on page 2 of this paper and include attached replacement sheets.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.

                                 An **Appendix** including amended drawing figures is attached following page 8 of this paper.